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[The stanford FLASH multiprocessor-](#) [psu.edu](#) (pdf)

J Kuskun, D Ofelt, M Heinrich, J Heinlein, R ... - International Symposium on Computer Architecture, 1998 - portal.acm.org

... the avoidance of extra copying of message **data**. ... low-level messages MAGIC sends **between** nodes, this ... consists of three phases: initiation, **transfer**, and reception ...

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[eNVy: a non-volatile, main memory storage system](#)

M Wu, W Zwaenepoel - Proceedings of the sixth international conference on ..., 1994 - portal.acm.org

... done to overcome the deficiencies associated with **Flash** ... times, creating a discrepancy **between** computation time ... available I/O rates by **transferring data** in par- ...

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[... memory cards using flash EEPROM integrated circuit chips and memory-controller ...](#)

RF Wallace, RD Norman, E Harari - US Patent 7,355,874, 2008 - Google Patents

... 23, 2006 Related US Application **Data** (60) Division ... with a large number of **flash** EEPROM integrated ... common **controller** circuit that interfaces **between** the **memory** ...

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[... address counter for synchronously transferring data between data ports and buffer memory](#)

MC Stolowitz - US Patent 6,018,778, 2000 - Google Patents

... ARRAY **CONTROLLER** FOR READING/Writing STRIPED **DATA** USING A SINGLE ADDRESS COUNTER FOR SYNCHRONOUSLY **TRANSFERRING DATA BETWEEN DATA PORTS AND BUFFER MEMORY** [75 ...

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[Integration of message passing and shared memory in the Stanford FLASH multiprocessor-](#)

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J Heinlein, K Gharachorloo, S Dresser, A ... - Proceedings of the sixth international conference on ..., 1994 - portal.acm.org

... can be further enhanced by avoiding message copying through direct **transfer** of **data** **between** user address ... interaction of message **data** with cache coherence. ...

Cited by 101 - [Related articles](#) - [BL Direct](#) - [All 10 versions](#)

[Magnetoresistive random access memory using magnetic tunnel junctions](#)

S Tehrani, JM Slaughter, M Deherra, BN ... - Proceedings of the IEEE, 2003 - ieeexplore.ieee.org

... circuit such as a processor or **controller**. ... by eliminating the need for **transferring data between** multiple memories ... The capability of **Flash memory** to store **data** ...

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[A flash-memory based file system-](#) [smallworks.com](#) (pdf)

A Kawaguchi, S Nishioka, H Motoda - [usenix.org](#)

... because the concurrent write operations can provide a higher **transfer** rate ... SRAM for write buffering, a large-bandwidth parallel **data** path **between** them, and ...

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[System architecture directions for networked sensors-](#) [ucla.edu](#) (pdf)

Tempest and Typhoon: User-level shared **memory**- [► wisc.edu](#) porj  
SK Reinhardt, JR Larus, DA Wood - Proceedings of the 21ST annual international ..., 1994 - portal.acm.org  
... both reads and writes, and **transfer** control to user ... **memory** involves replicat- ing  
remote **data** to ensure ... are local and maintaining coherence **between** the multiple ...  
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